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Date: August 15, 2006

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Confirmation No. 1091

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RE: AMENDMENT for Serial No. 10/773,276 (Tomoo KIMURA et al.), filed February 9, 2004

MESSAGE:

ATTACHED HEREWITH ARE THE FOLLOWING ITEMS:

1. Amendment (15 pages)

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Kenneth W. Fields

arne of person signing certification

Kunst W. Frell

August 15, 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Confirmation No. 1091

Tomoo KIMURA et al.

Attorney Docket No. 2004_0156A

Serial No. 10/773,276

Group Art Unit 2825

Filed February 9, 2004

Examiner Vuthe Siek

LOGIC CIRCUIT OPTIMIZING METHOD,

LOGIC CIRCUIT OPTIMIZING DEVICE

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AND LOGIC CIRCUIT COMPOSING DEVICE

AMENDMENT

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Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In response to the Office Action of May 15, 2006, please amend the above-referenced U.S. patent application as follows: